



# 7<sup>th</sup> International Conference on Design & Technology of Integrated Systems in Nanoscale Era

May 16-18, 2012 Gammarth, Tunisia  
<http://www.dtis-conference.net/>

## General Co-Chairs:

### General scope:

#### Integrated System Design :

- SOC, SIP design
- Multiprocessor systems
- Embedded systems
- Wireless systems
- Network on Chip
- Analog, Mixed Signal and RF systems
- Digital Design Automation Technics
- MEMS and MOEMS systems
- Low Voltage and Low Power systems
- Innovative technologies
- Synthesis (physical, logic,...)
- Simulation, Validation and Verification

#### Integrated System Technology :

- Device modeling
- Material characterization
- Failure analysis
- New components
- Packaging
- Process technology
- Reliability issues

#### Integrated System Testing :

- Defect and fault modeling
- Analog and Mixed Signal testing
- MEMS/MOEMS testing
- SOC and SIP testing
- Delay testing
- Memory testing
- Fault Simulation, ATPG
- DFT, BIST and BISR
- On-line testing and fault tolerant systems
- ATE issues

### Papers Submission:

Authors are invited to submit original, unpublished research work for the DTIS'12. Paper submissions should be complete manuscripts. The paper must clearly indicate the area, main results and contributions. The manuscript should follow the instructions given in the author's kit of the conference web site. On the title page, please include: author name(s) and affiliation(s), mailing address and contact author e-mail. All submissions should be as MSWord or PDF files and have to be made electronically through the DTIS'12 on-line submission system.

A submission will be considered as evidence that, upon acceptance, an author or coauthor will prepare the final version in due time and also a commitment that he or she will register, attend the meeting and present the paper during the conference. Accepted and presented papers will appear on the IEEE Xplore.

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The aim of the 7<sup>th</sup> International conference on Design & Technology of Integrated Systems in nanoscale era (DTIS'12) is to cope with the rapidly progressing technology which reaches today the nanometer scale. The general area of interest includes the design, test and technology of electronic products, ranging from integrated circuits modules and printed circuit board to full systems and Microsystems, as well as examining the methodologies and tools used in the design, verification and validation of such products. DTIS'12 will be organized by The National Engineering School of Sfax in collaboration with the Laboratory of Informatics, Robotics and Microelectronics of Montpellier (LIRMM), France. It will be supported by The Tunisian Ministry of Higher Education and Scientific Research, and technically co-sponsored by the IEEE and the IEEE Circuits and Systems Society. The conference official language is English.

This 7<sup>th</sup> edition of the DTIS conference will be held in Gammarth (Tunisia), located in the Tunisian North. Gammarth offers visitors an air of freshness and sweetness of life. It is well known tourist city with luxury hotels and charming beaches.



### Important Dates:

- January 31, 2012 : Submission deadline
- February 27, 2012 : Notification of acceptance
- March 26, 2012 : Final version due date